

S/N 10/628,769

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Suresh Marisetty et al. Examiner: Gabriel Chu
Serial No.: 10/628,769 Group Art Unit: 2114
Filed: July 28, 2003 Docket: 884.205US2
Title: SYSTEM ABSTRACTION LAYER, PROCESSOR ABSTRACTION LAYER,
AND OPERATING SYSTEM ERROR HANDLING

TERMINAL DISCLAIMER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I, Charles E. Steffey, am the attorney of record for the above identified patent application as evidenced by the Power of Attorney filed in the present application on July 28, 2003. I am making this petition on behalf of Intel Corporation, the assignee of the present invention. As the attorney of record, I am empowered to act on behalf of the assignee and, in accordance with 37 C.F.R. § 1.321(b)(iv), to sign this terminal disclaimer.

Certificate Under 37 C.F.R. § 3.73(b)

Your petitioner, Intel Corporation, certifies that they are the owner of the entire right, title and interest in and to the above-identified patent application (Serial No. 10/628,769) and to U.S. Patent No. 6,622,260, to U.S. Patent No. 6,675,324, to U.S. Patent No. 7,216,252, U.S. Application Serial No. 10/628,726, and to U.S. Application Serial No. 10/672,697.

Your petitioner owns the entire right, title, and interest of these applications by nature of the assignments executed and filed for these applications.

The assignment for U.S. Patent No. 6,622,260 was recorded on April 24, 2000 on Reel 010777, Frames 0590 - 0598, with the United States Patent and Trademark Office. The above-identified patent application (Serial No. 10/628,769) is a continuation of U.S. Patent No. 6,622,260.

The assignment for U.S. Patent No. 6,675,324 was recorded on September 27, 1999 on Reel 010275, Frames 0682-0688, with the United States Patent and Trademark Office. U.S. Application Serial No. 10/628,726 is a continuation of U.S. Patent No. 6,675,324.

The assignment for U.S. Patent No. 7,216,252 was recorded in its parent application (U.S. Patent No. 6,684,346) on August 13, 2001 on Reel 012070, Frames 0082-0084, with the United States Patent and Trademark Office.

The assignment for U.S. Application Serial No. 10/672,697 was recorded on February 17, 2007 on Reel 014978, Frames 0537-0539, with the United States Patent and Trademark Office.

The undersigned representative of the assignee has reviewed the evidentiary documents of title and certifies that to the best of assignee's knowledge and belief, title is in the assignee, Intel Corporation, seeking to take the action set forth in this disclaimer.

Terminal Disclaimer

Your petitioner hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the above-identified patent application, which would extend beyond the expiration date of the full statutory term, as presently shortened by any terminal disclaimers, of U.S. Patent No. 6,622,260, U.S. Patent No. 6,675,324, U.S. Patent No. 7,216,252, U.S. Application Serial No. 10/628,726, and U.S. Application Serial No. 10/672,697. Petitioner hereby agrees that any patent so granted on the above-identified application shall be enforceable only for and during such period that the legal title to U.S. Patent No. 6,622,260, U.S. Patent No. 6,675,324, U.S. Patent No. 7,216,252, U.S. Application Serial No. 10/628,726, and U.S. Application Serial No. 10/672,697 and the legal title of the above-identified application and any patent granted thereon remain common, this agreement to run with any patent granted on the above-identified application and to be binding upon the grantee, its successors and assigns.

Limitations on the Disclaimer

Your petitioner does not disclaim any terminal part of any patent granted on the above-identified application prior to the expiration date of the full statutory term as presently shortened by any terminal disclaimer of U.S. Patent No. 6,622,260, U.S. Patent No. 6,675,324, U.S. Patent No. 7,216,252, U.S. Application Serial No. 10/628,726, and U.S. Application Serial No. 10/672,697 in the event that it later expires for failure to pay a maintenance fee, is held unenforceable, is found invalid, is statutorily disclaimed in whole or terminally disclaimed under 37 C.F.R. § 1.321(a), has all claims canceled by a reexamination certificate, or is otherwise

TERMINAL DISCLAIMER

Serial Number: 10/528,769

Filing Date: July 28, 2003

Title: SYSTEM ABSTRACTION LAYER, PROCESSOR ABSTRACTION LAYER, AND OPERATING SYSTEM ERROR HANDLING

Page 3
Dkt: 884.205 US2

terminated prior to the expiration date of its statutory term as presently shortened by any terminal disclaimer, except for the separation of legal title as stated hereinabove.

Fee Status

Please charge Deposit Account 19-0743 in the amount of \$130.00, which is required under 37 C.F.R. § 1.20(d) to file a statutory disclaimer. The Commissioner of Patents and Trademarks is hereby authorized to charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

SCHWEGMAN, LUNDBERG & WOESSNER, P.A.
P.O. Box 2938
Minneapolis, MN 55402
612-371-2132

Date

July 21, 2008

By


Charles E. Steffey
Reg. No. 25,179

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 21st day of July 2008.

Name

Amy Moriarty

Signature

